

IN THE CLAIMS

1. (Currently amended) A semiconductor device having a pad region and a circuit region, comprising:
 - a low-k dielectric film on the pad region and the circuit region of a substrate, the low-k dielectric film having a dielectric constant of no more than 3;
 - an insulating film ~~formed~~ interposed in ~~[[on]]~~ the low-k dielectric film of the pad region, the insulating film having higher strength than the low-k dielectric film;
 - multi-layer wirings ~~formed~~ interposed in ~~[[on]]~~ the insulating film of the pad region and interposed in ~~[[on]]~~ the low-k dielectric film of the circuit region; and
 - a bonding pad on an outermost wiring of the multi-layer wirings of the pad region.
2. (Previously presented) The semiconductor device according to claim 1, wherein sidewalls of the wirings in the pad region are surrounded by the insulating film.
3. (Previously presented) The semiconductor device according to claim 1, wherein the low-k dielectric film is an insulating film containing silicon, carbon, oxygen, and hydrogen.
4. (Currently amended) A semiconductor device having a pad region and a circuit region, comprising:
 - multi-layer low-k dielectric films on the pad region and the circuit region of a substrate, each of the multi-layer low-k dielectric films having a dielectric constant of no more than 3;
 - insulating films formed in ~~[[on]]~~ each of the multi-layer low-k dielectric films of the pad region, each of the insulating films having higher strength than each of the multi-layer low-k dielectric films;
 - wirings formed in ~~[[on]]~~ each of the insulating films of the pad region and in ~~[[on]]~~ each of the low-k dielectric films of the circuit region; and
 - a bonding pad on an outermost wiring of the wirings of the pad region.
5. (Previously presented) The semiconductor device according to claim 4, wherein sidewalls of the wirings in the pad region are surrounded by the insulating films.

Claims 6-9 (cancelled)

10. (Previously presented) The semiconductor device according to claim 1, wherein the low-k dielectric film is a polymer film containing hydrogen and carbon.

11. (New) The semiconductor device according to claim 1, wherein sidewalls of the wirings in the pad region are in contact with the insulating film.